Docket No.:

217713US2



IN RE APPLICATION: Jun TATSUTA, et al.

SERIAL NO.: 10/026,639

GAU: 2827

FILED: December 27, 2001

EXAMINER: CRUZ, L.

FOR: SEMICONDUCTOR-CHIP MOUNTING SUBSTRATE AND METHOD OF

MANUFACTURING THE SAME

LETTER SUBMITTING DRAWING REPLACEMENT SHEET(S)

COMMISSIONER FOR PATENTS Alexandria, VA 22313

SIR:

Responsive to the below indicated communication, 1 drawing replacement sheets are submitted herewith.

Official Act	ion dated March 11, 2003
Notice of Allowance/Issue Fee dated	
Other	dated

The changes and/or modifications made include the following:

To remedy deficiencies noted in page 2 of the Office Action Summary

Respectfully submitted,

Gregory J. Maier

Registration No. 25,599

Christopher D. Ward Registration No. 41,367

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